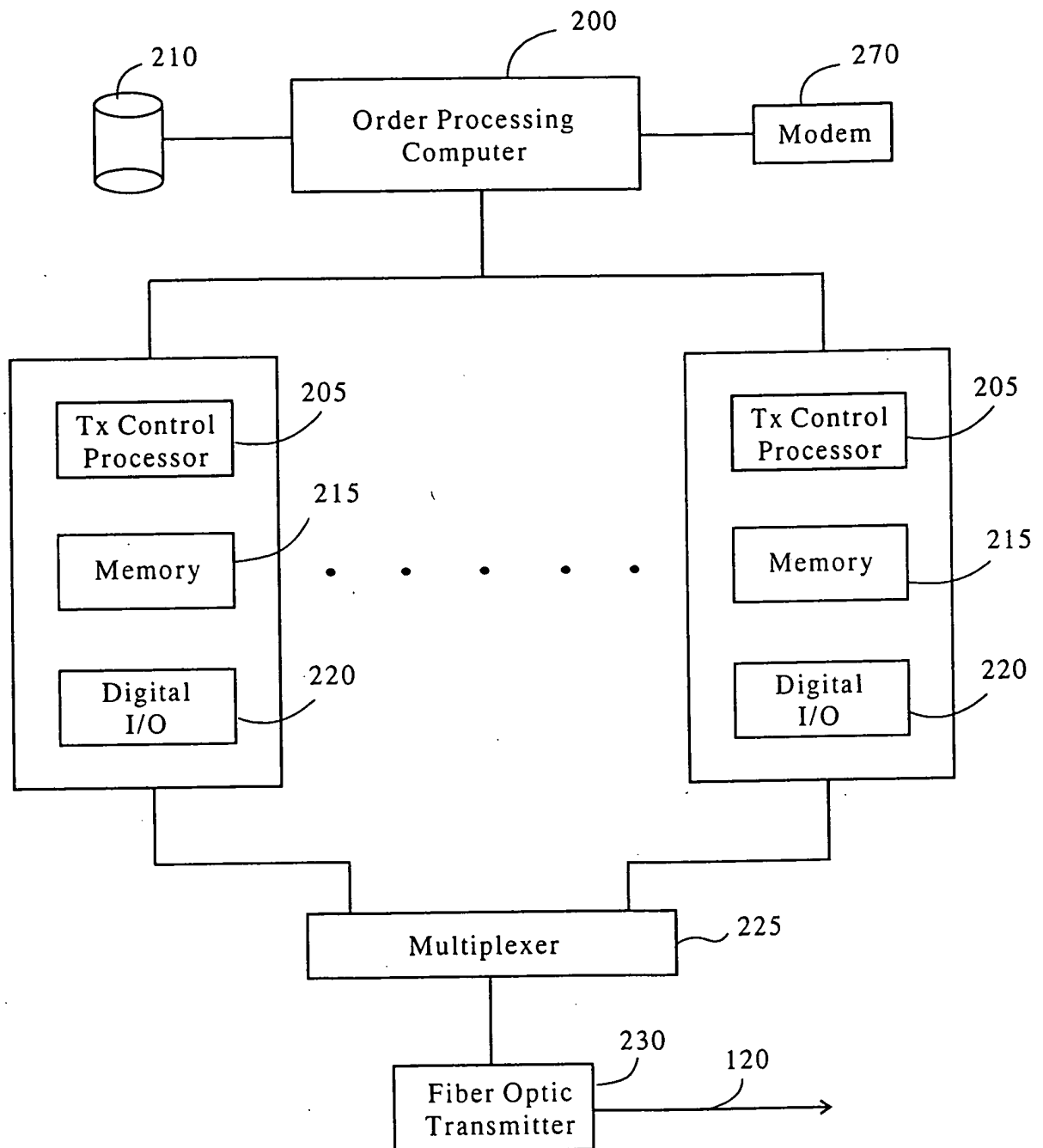
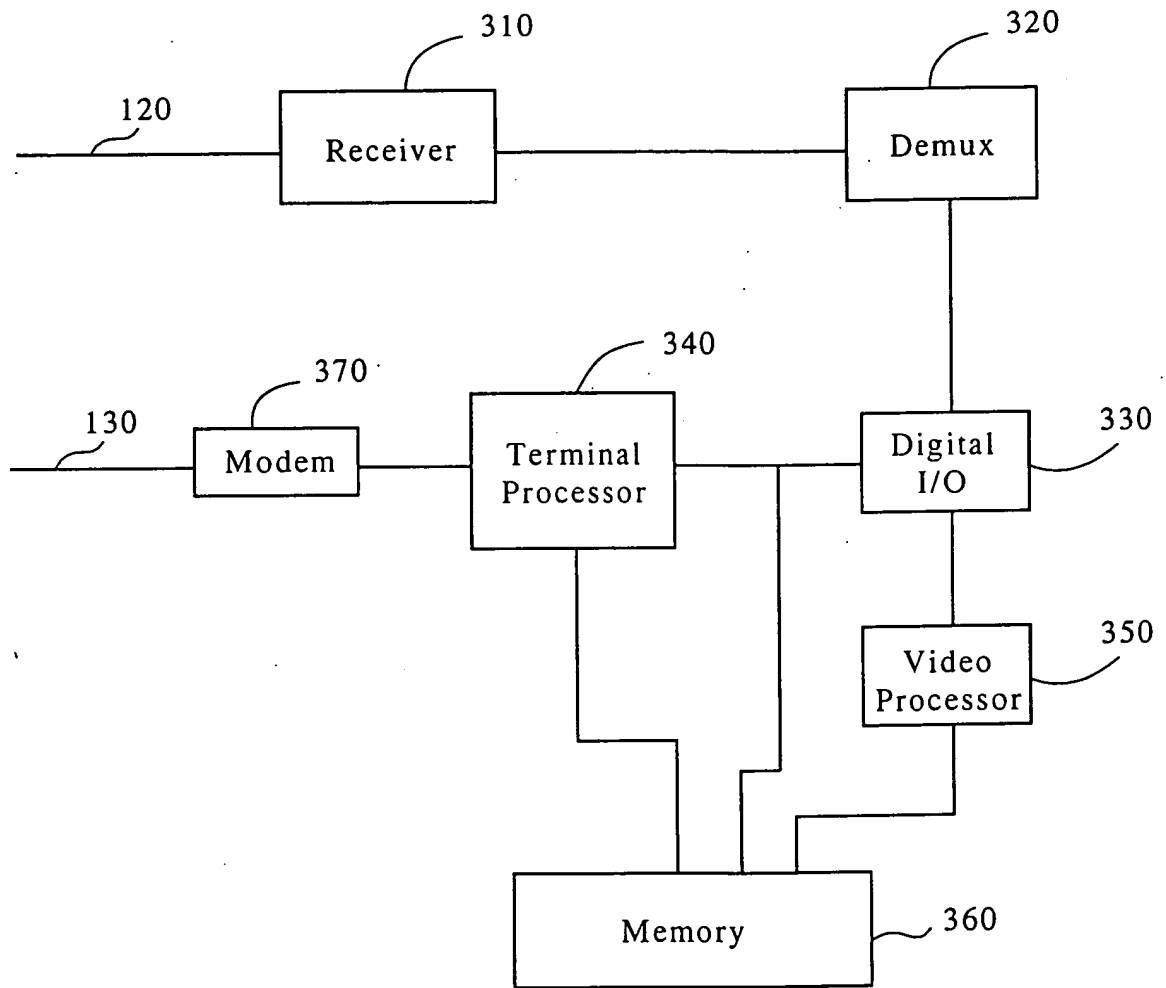
**FIG. 1**

**FIG. 2**

**FIG. 3**

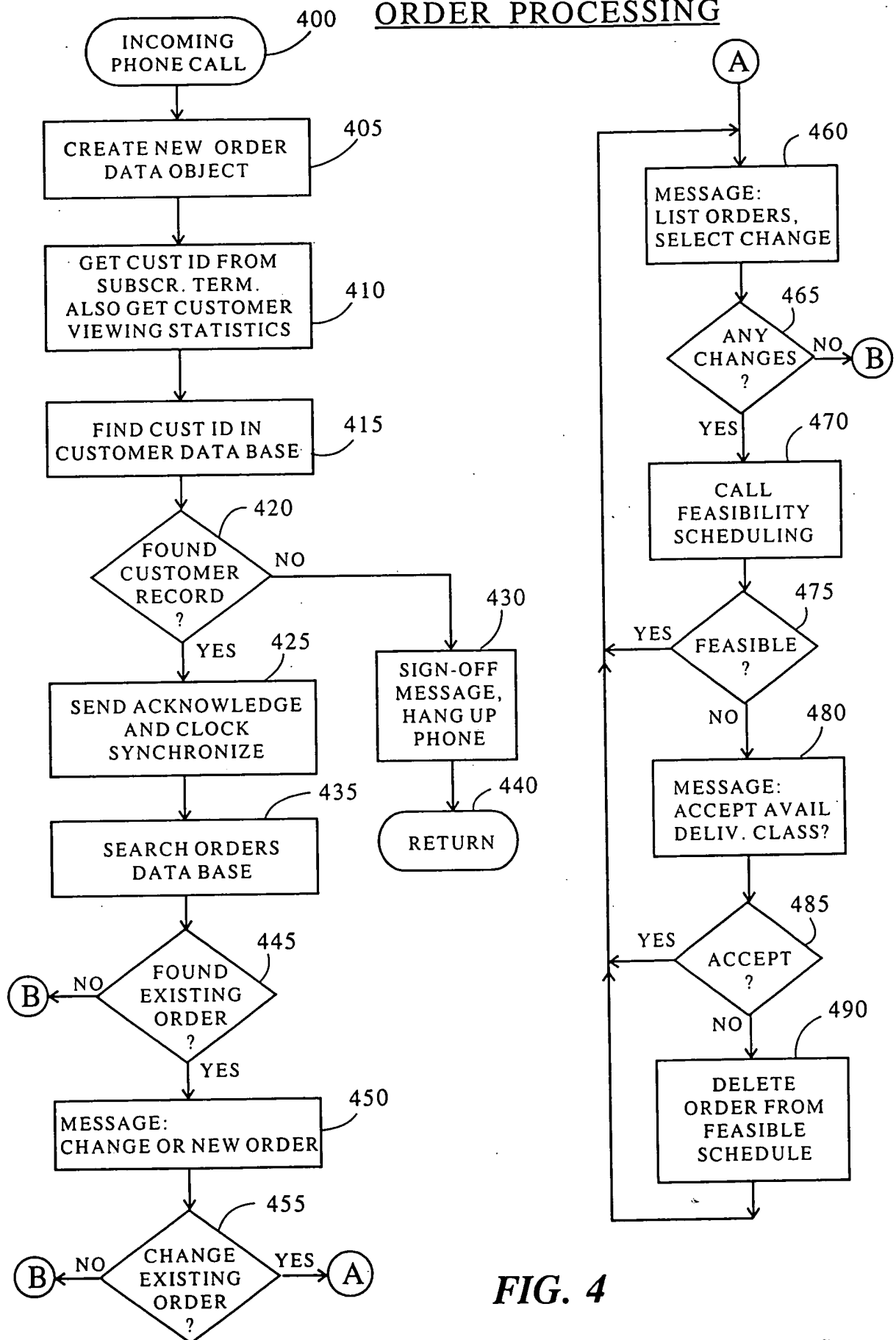
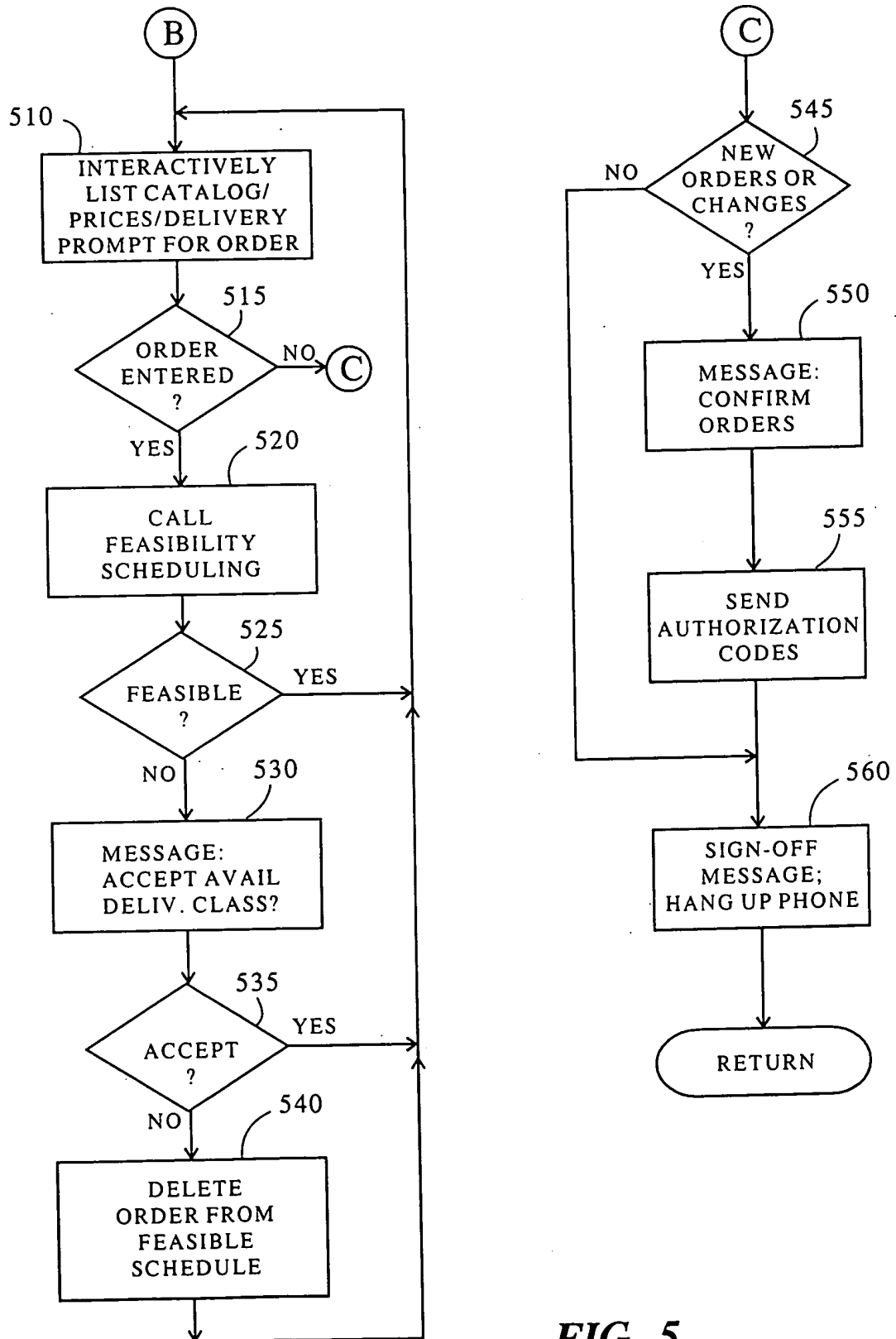
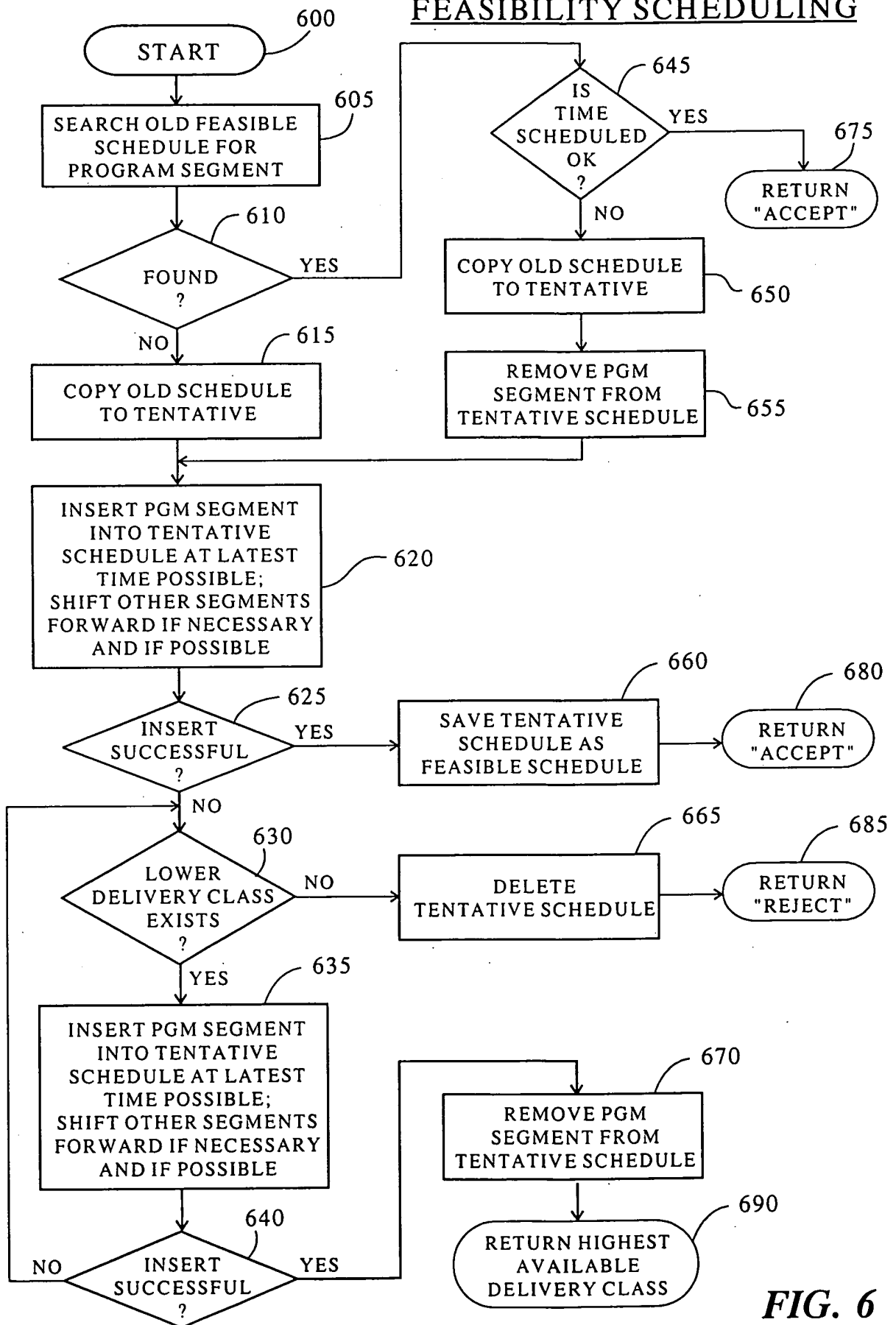
ORDER PROCESSING

FIG. 4

ORDER PROCESSING (CONT.)**FIG. 5**

FEASIBILITY SCHEDULING**FIG. 6**

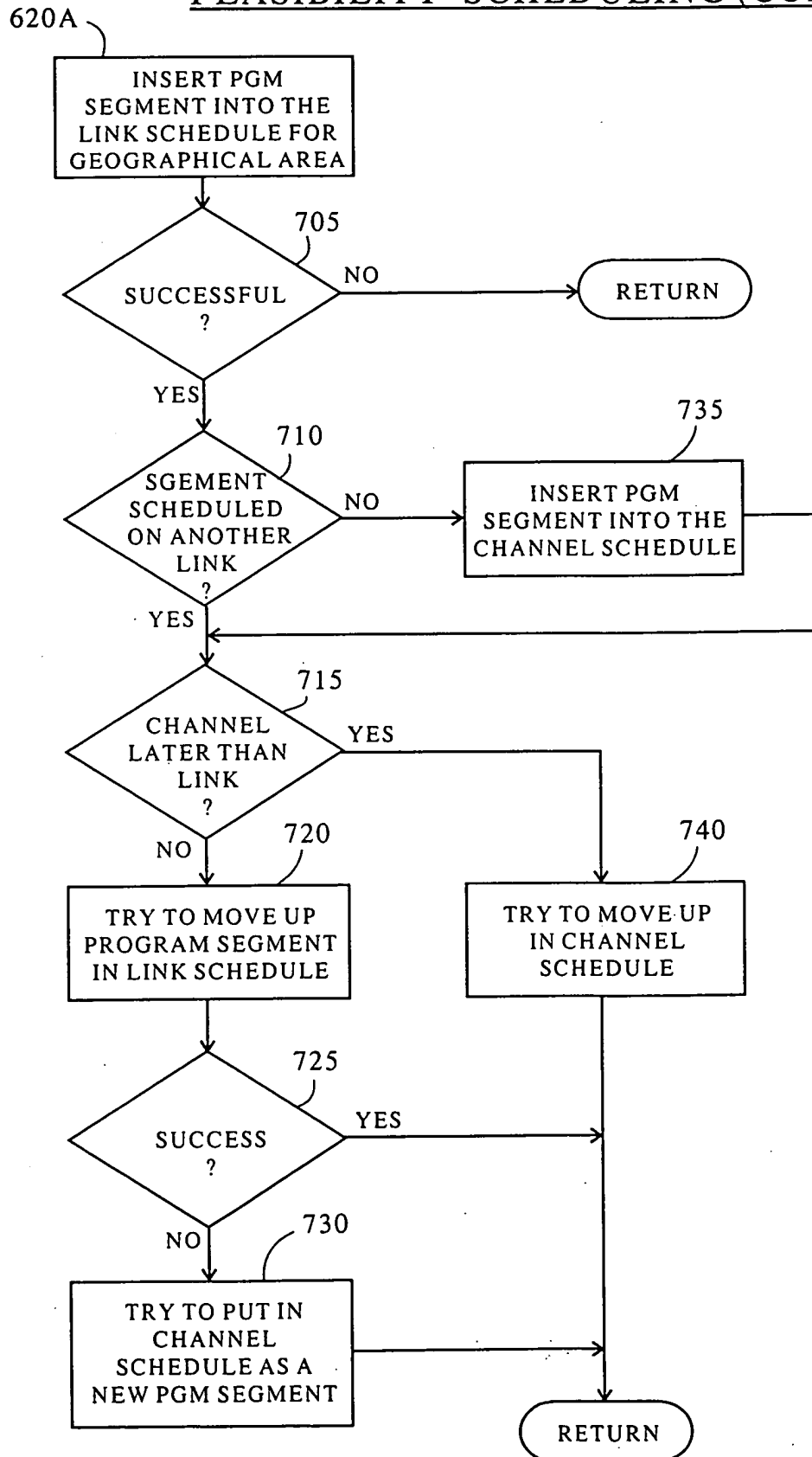
FEASIBILITY SCHEDULING (CONT.)

FIG. 7

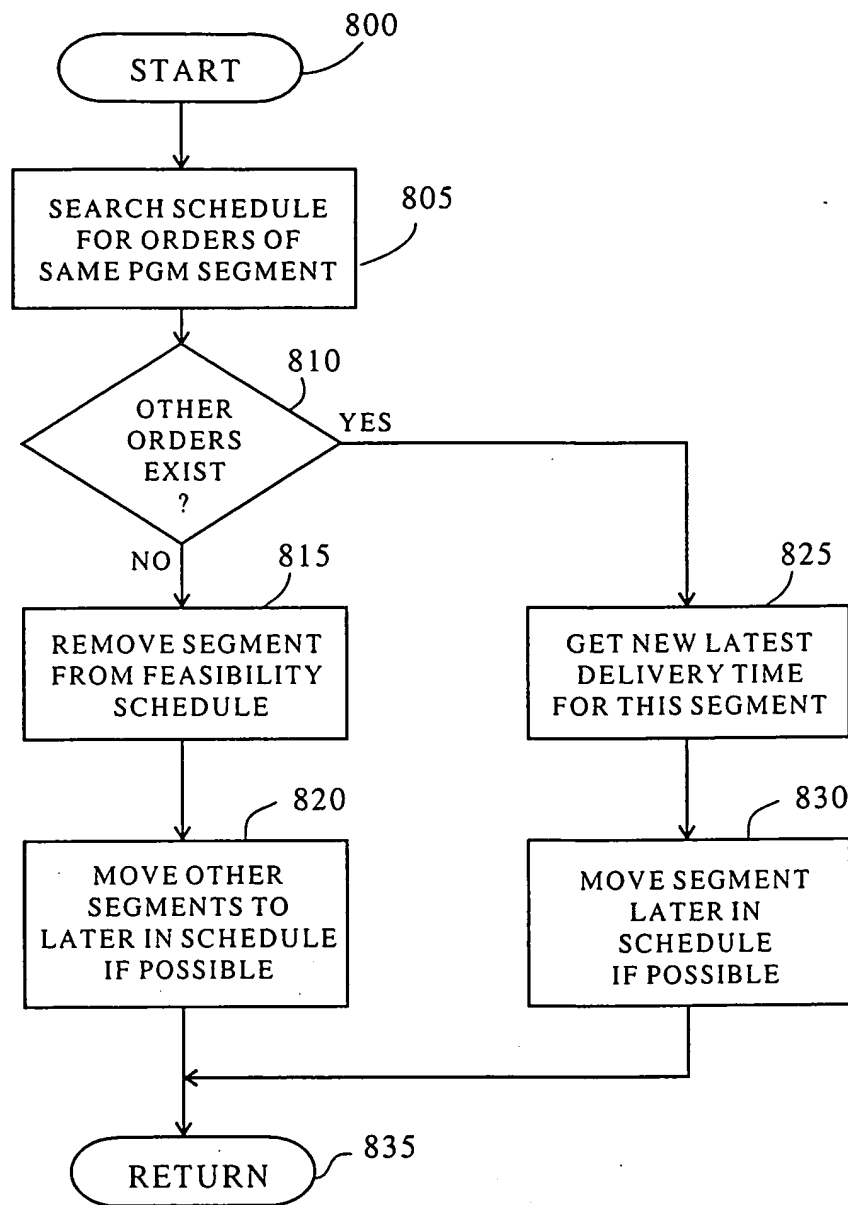
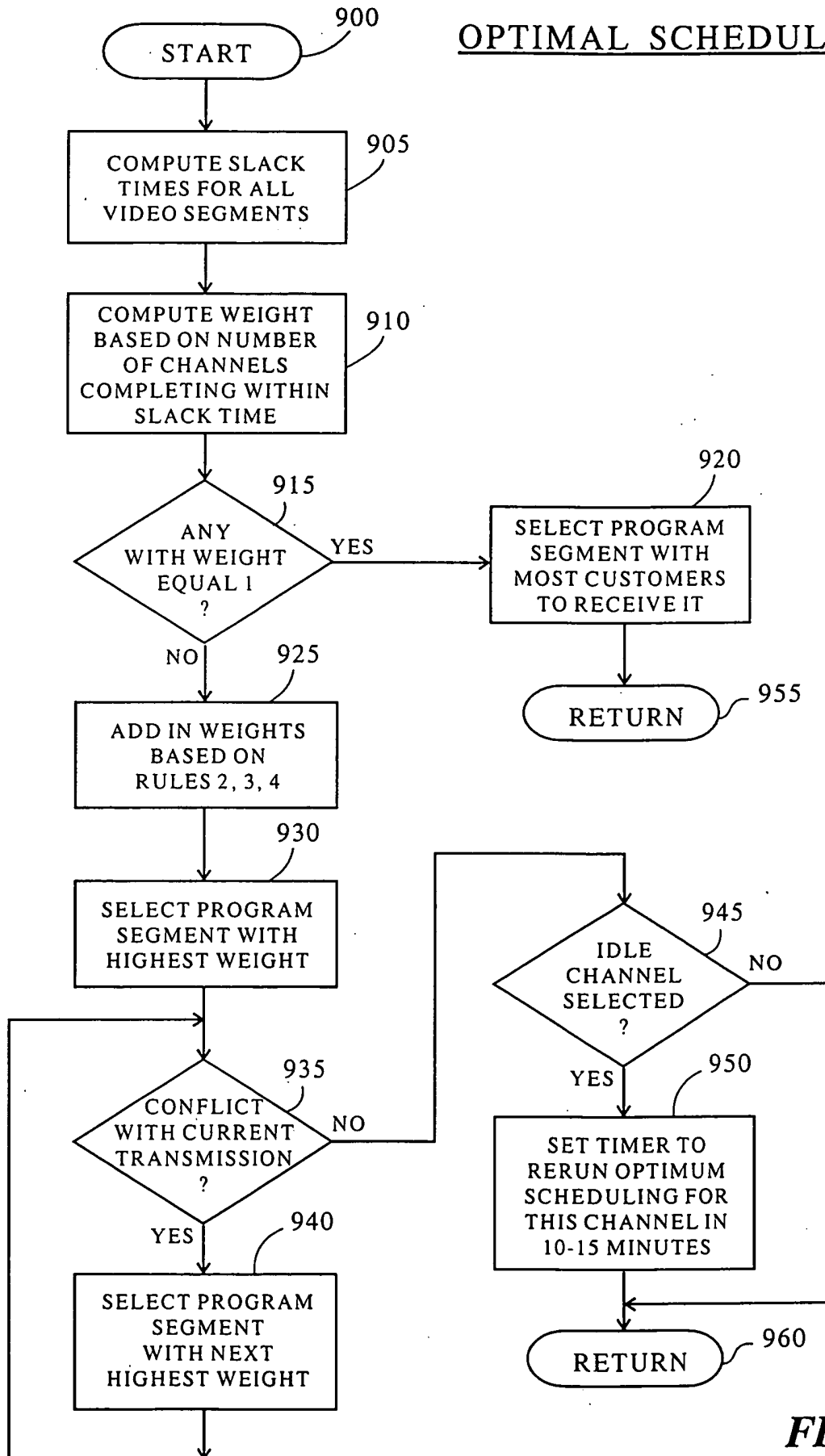


FIG. 8

OPTIMAL SCHEDULING**FIG. 9**

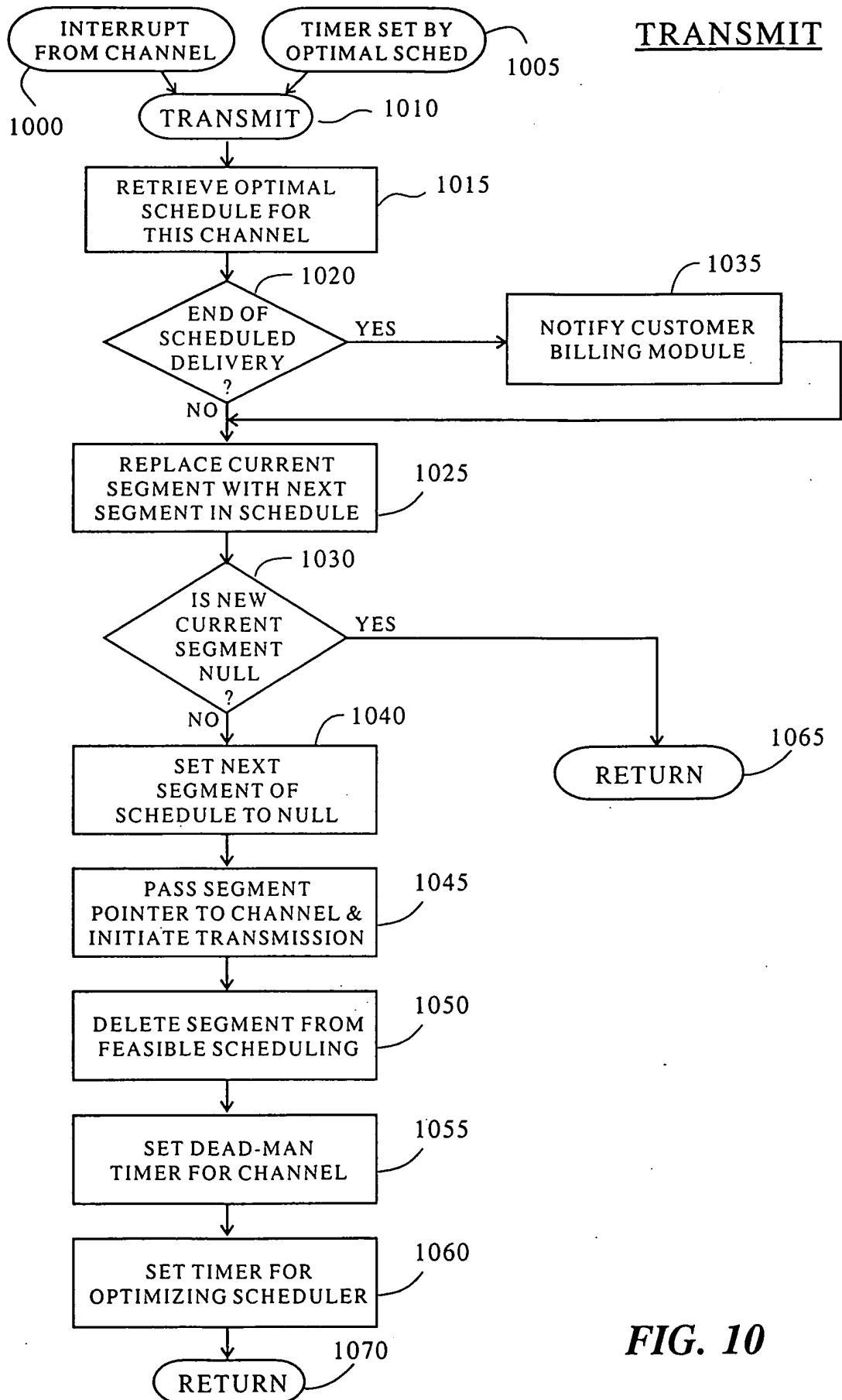
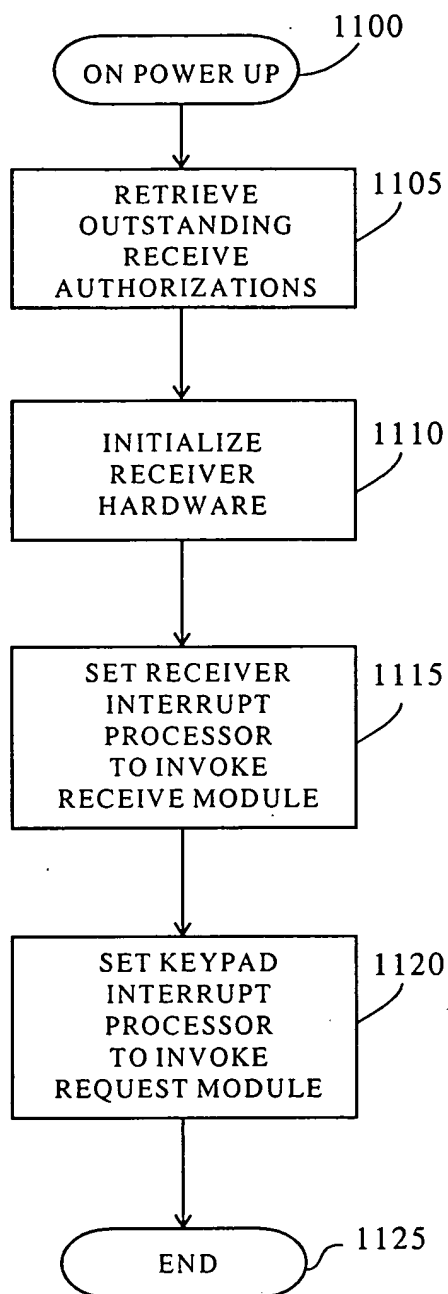
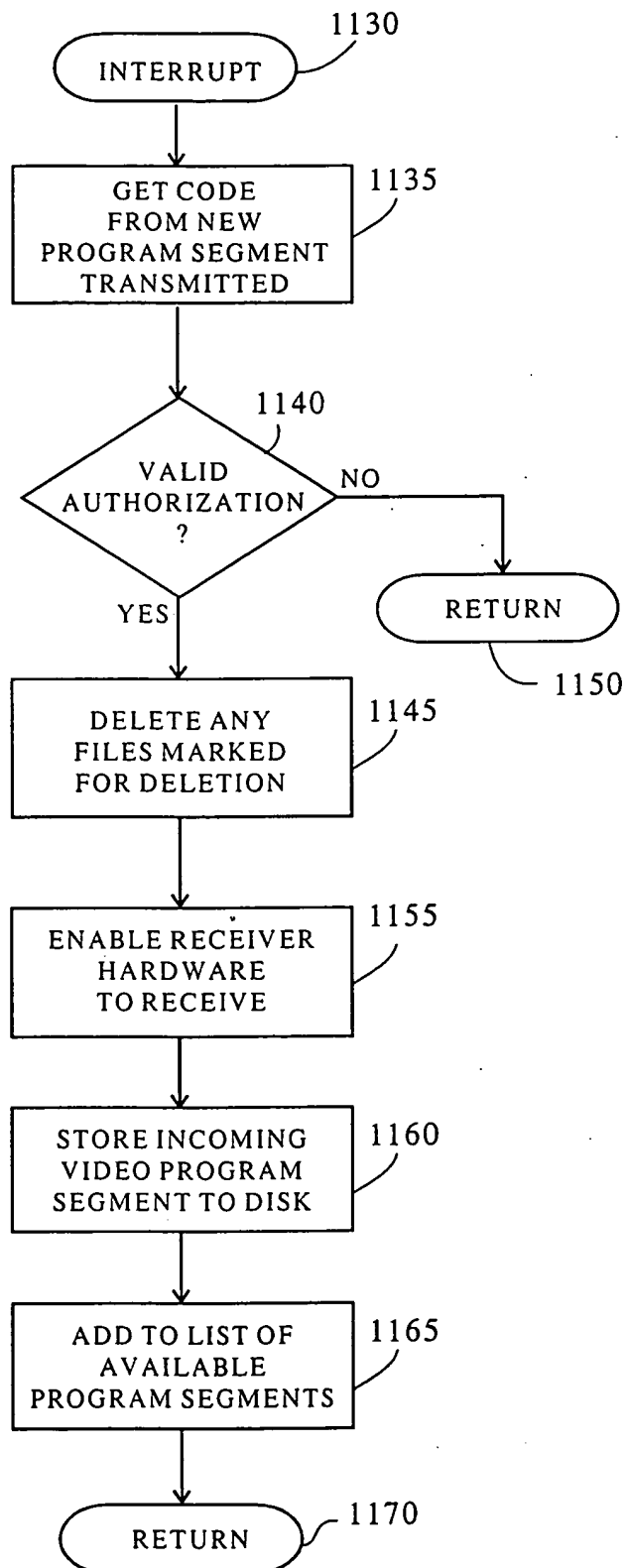


FIG. 10

START UP MODULERECEIVE MODULE**FIG. 11**

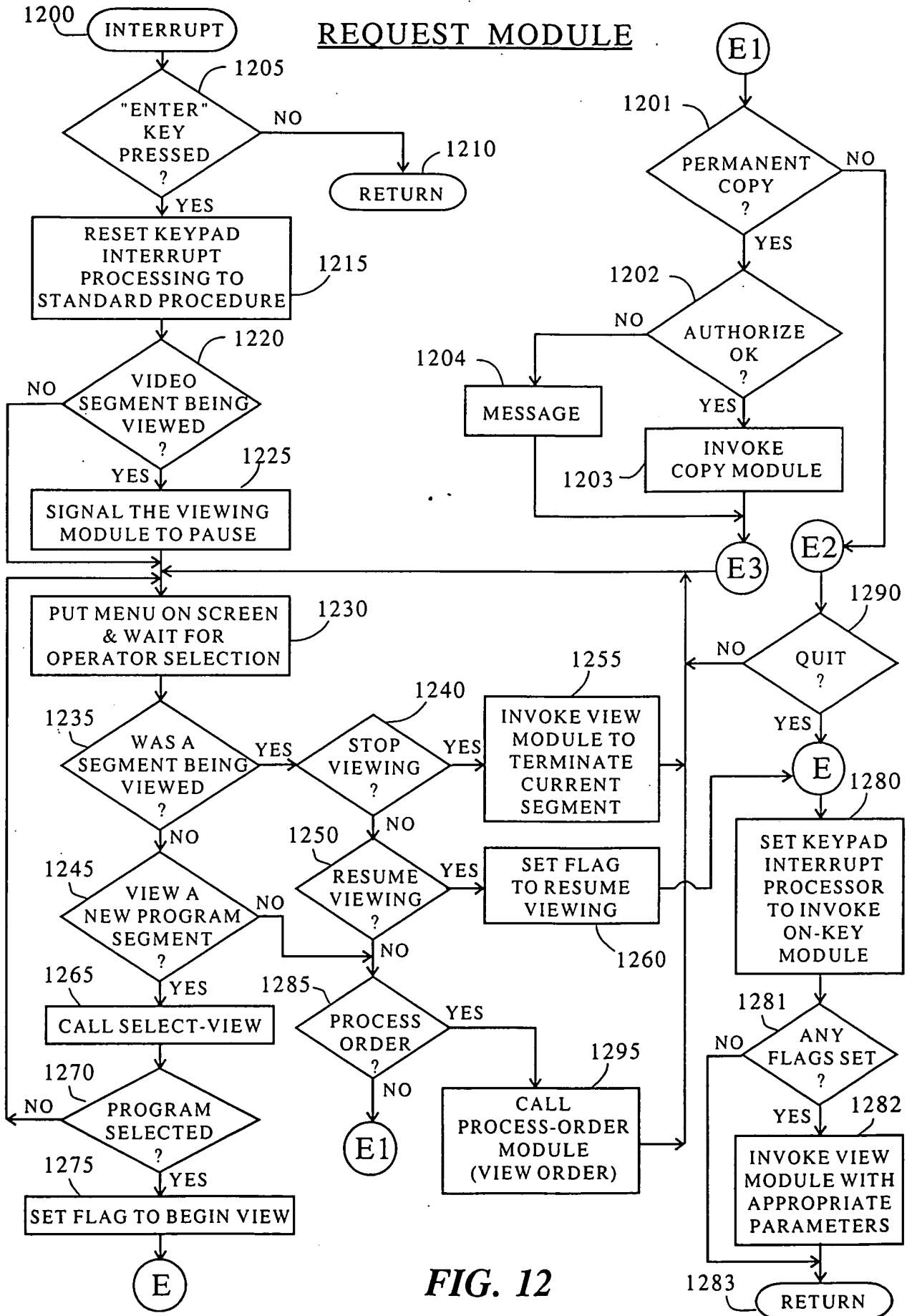


FIG. 12

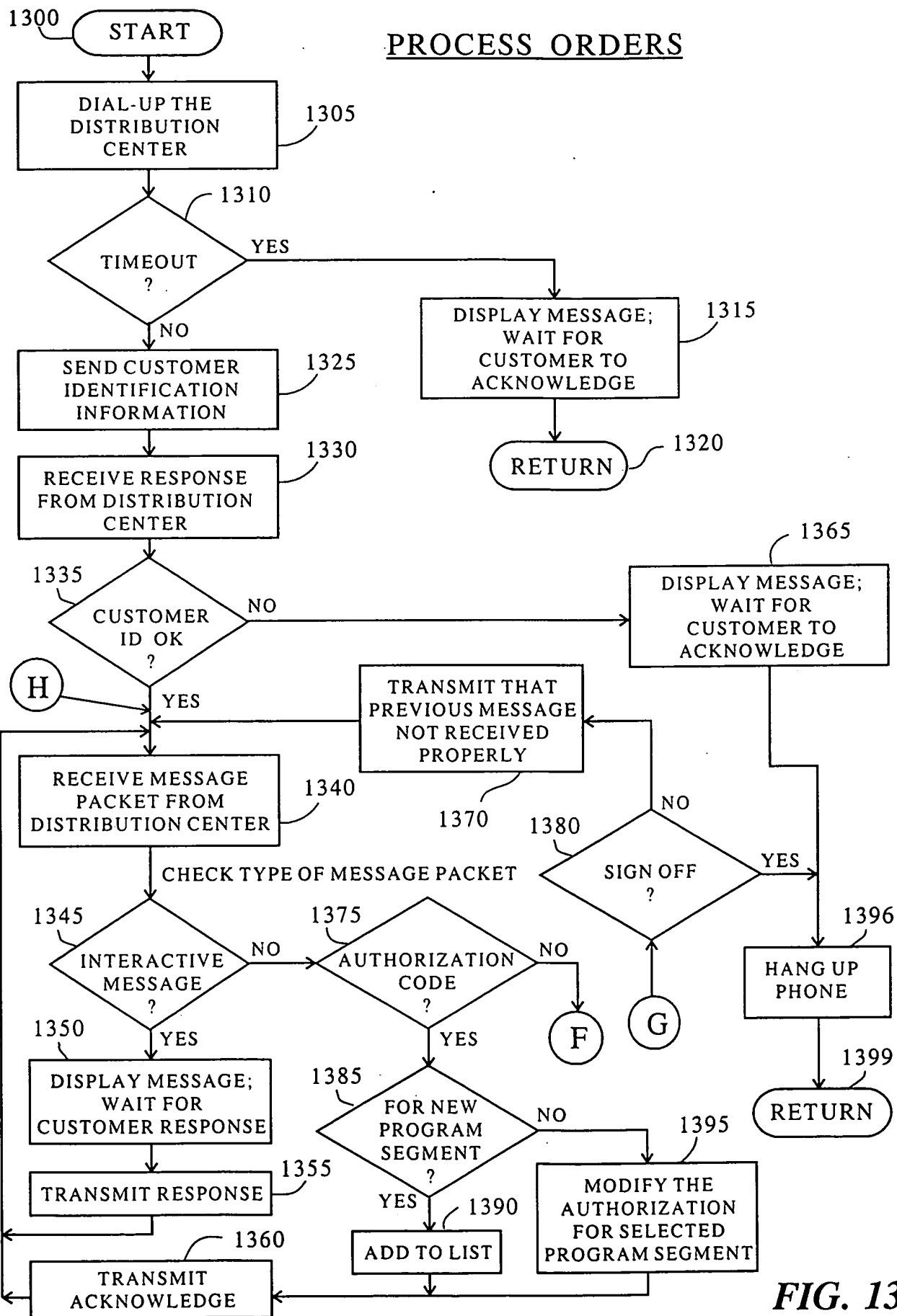
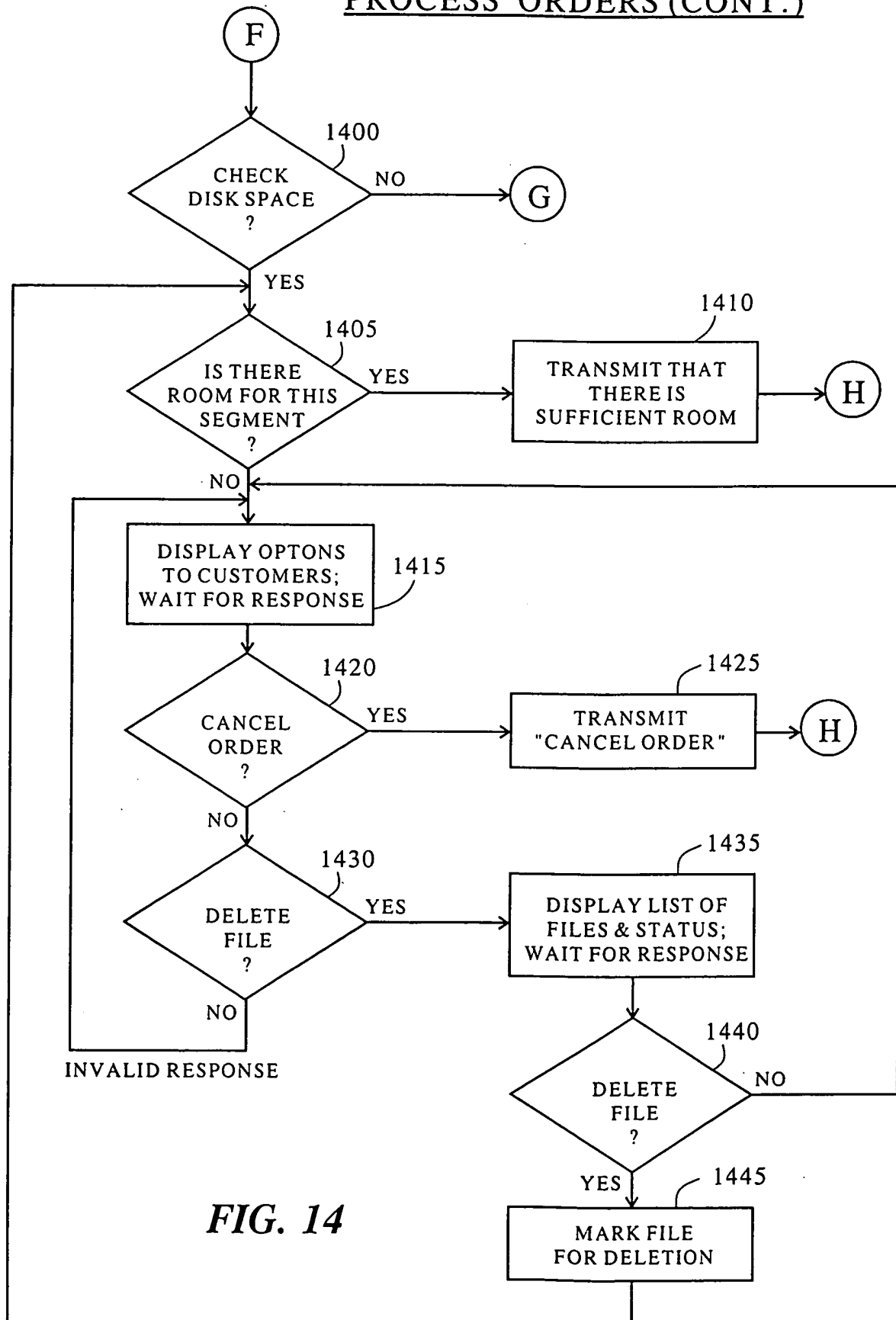
PROCESS ORDERS

FIG. 13

PROCESS ORDERS (CONT.)**FIG. 14**

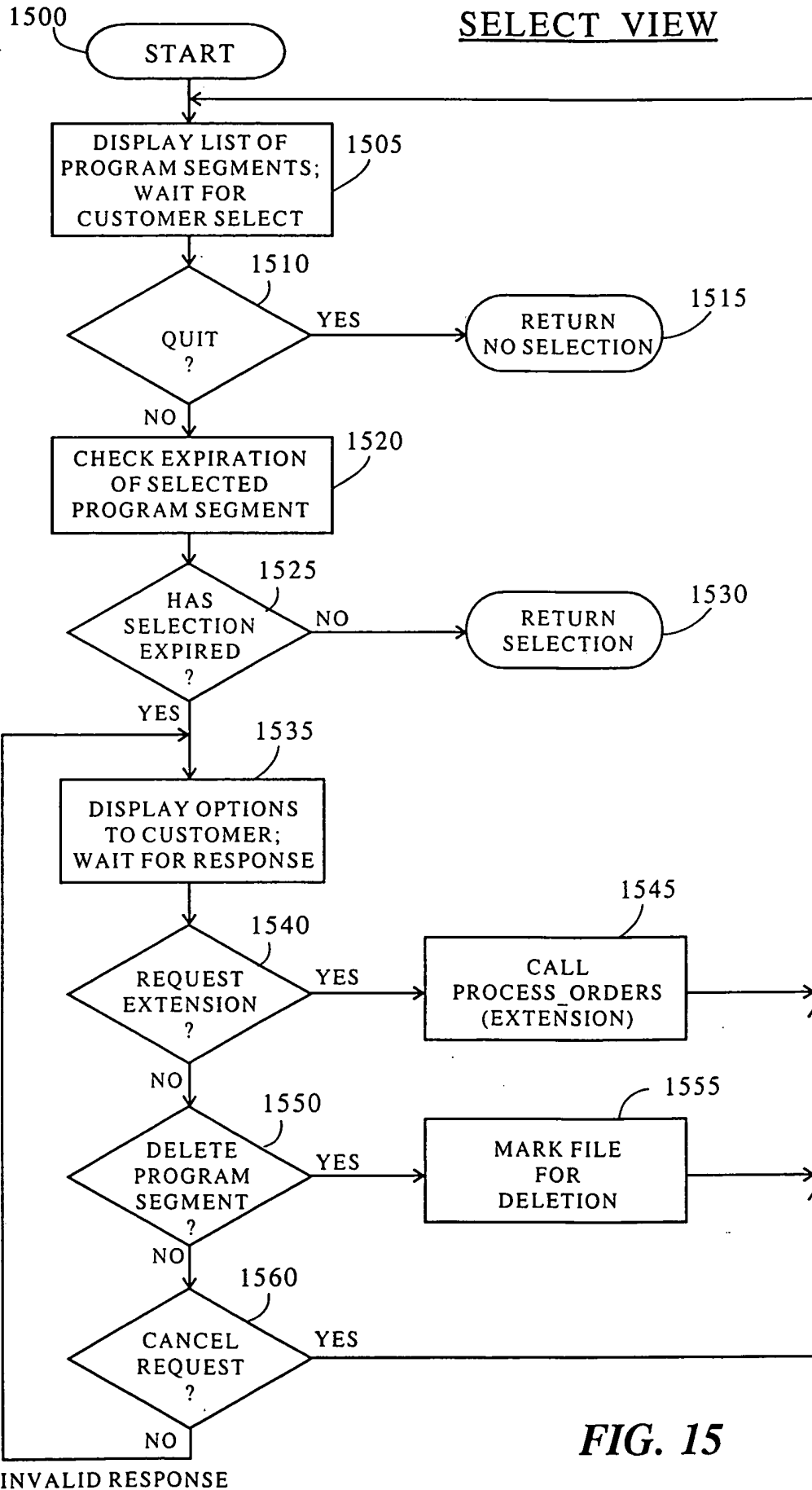
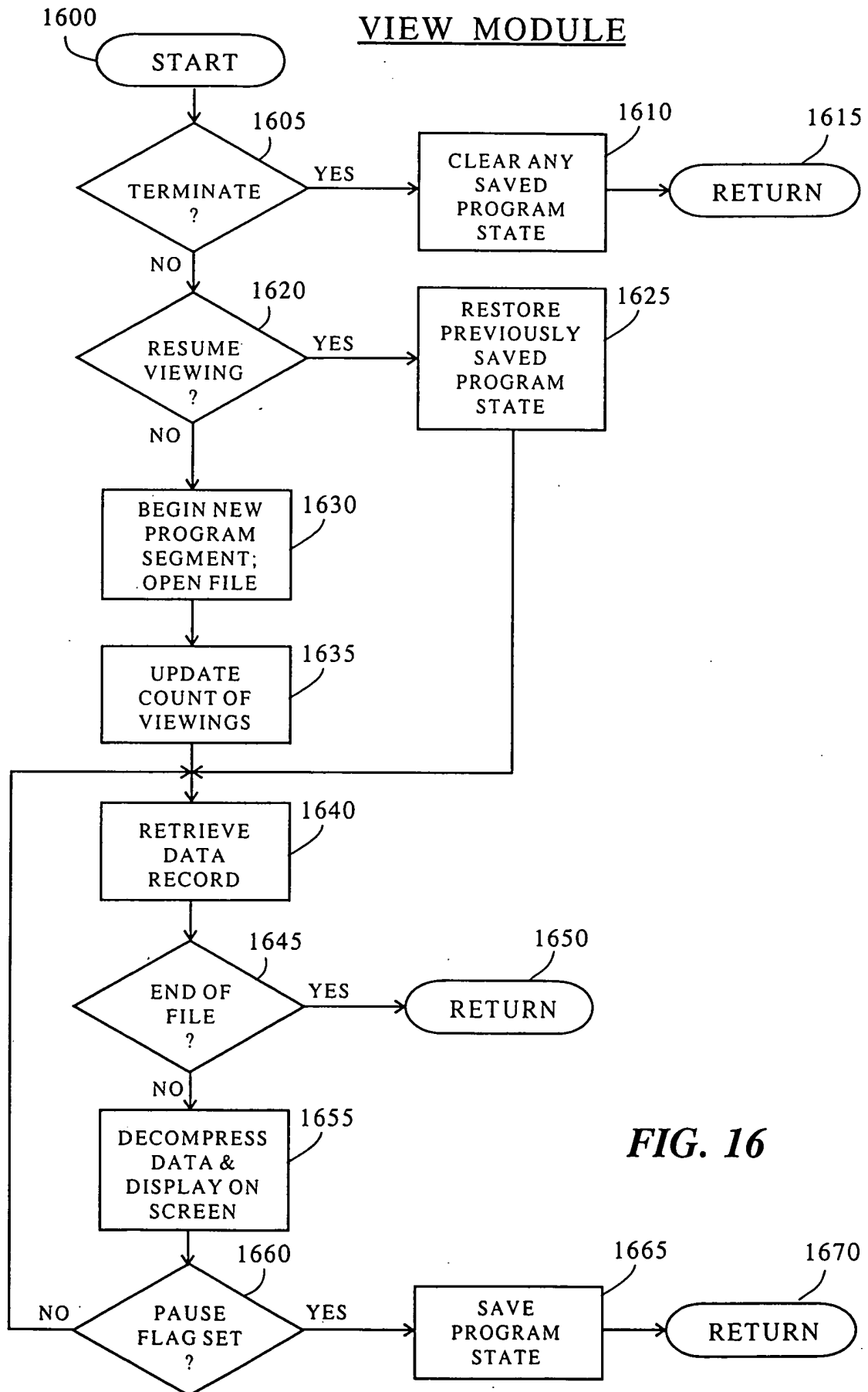


FIG. 15

**FIG. 16**